JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD
HYDERABAD - 500 085, ANDHRA PRADESH, INDIA

CONSOLIDATED MARKS MEMO / CREDIT SHEET

BACHELOR OF TECHNOLOGY- ELECTRONICS & COMMUNICATION ENGINEERING

CMM. No. : **C 0446168**

Serial No. : 21101027371

Name : SHAIKH SAMEER KHALIL

Hall Ticket No. : 115A5A0401

Name of the College : 5A-TMITS, INDRESHAM

Month & Year of Final Exam : April, 2014

Year of Admission : 2011-2012

Class Awarded : FIRST CLASS WITH DISTINCTION

S.No.	SUBJECT TITLE	INT MARKs	EXT MARKS	TOTAL	CREDITS	S.No.	SUBJECT TITLE	INT MARKs	EXT MARKS	TOTAL	CREDITS
	Maximum Marks in Theory	25	75	100			Maximum Marks in Lab	25	50	75	

I YEAR

DIRECT ADMISSION INTO II-YEAR	UNDER LATERAL ENTRY SCHEME

II YEAR

S.No.	I SEMESTER	INT	EXT	TOTAL	CREDITS	S.No.	II SEMESTER	INT	EXT	TOTAL	CREDITS
1	MATHEMATICS-III	19	35	54*	3	1	PRINCIPLES OF ELECTRICAL ENGINEERING	23	49	72	3
2	PROBABILITY THEORY & STOCHASTIC PROCESSES	24	31	55	3	2	ELECTRONIC CIRCUIT ANALYSIS	16	56	72	4
3	ENVIRONMENTAL STUDIES	23	39	62	3	3	PULSE & DIGITAL CIRCUITS	24	49	73	4
4	ELECTRIC CIRCUITS	22	52	74	4	4	SWITCHING THEORY & LOGIC DESIGN	24	52	76	4
5	ELECTRONIC DEVICES & CIRCUITS	22	45	67	4	5	ELECTROMAGNETIC THEORY & TRANSMISSION LINES	21	37	58	4
6	SIGNALS & SYSTEMS	21	43	64	4	6	ELECTRICAL ENGINEERING LAB	24	49	73	2
7	ELECTRONIC DEVICES & CIRCUITS LAB	24	43	67	2	7	ELECTRONIC CIRCUIT ANALYSIS LAB	20	48	68	2
8	BASIC SIMULATION LAB	24	42	66	2	8	PULSE & DIGITAL CIRCUITS LAB	23	49	72	2

III YEAR

S.No.	I SEMESTER	INT	EXT	TOTAL	CREDITS	S.No.	II SEMESTER	INT	EXT	TOTAL	CREDITS
1	CONTROL SYSTEMS	24	33	57	4	1	MANAGERIAL ECONOMICS & FINANCIAL ANALYSIS	21	47	68	4
2	COMPUTER ORGANIZATION	24	41	65	4	2	OPERATING SYSTEMS	21	38	59	4
3	ANTENNAS & WAVE PROPAGATION	24	64	88	3	3	DIGITAL COMMUNICATIONS	21	60	81	3
4	ELECTRONIC MEASUREMENTS & INSTRUMENTATION	23	50	73	4	4	MICROPROCESSORS & MICROCONTROLLERS	23	27	50*	4
5	ANALOG COMMUNICATIONS	22	39	61	3	5	DIGITAL SIGNAL PROCESSING	20	50	70	4
6	IC APPLICATIONS	22	50	72	3	6	MICROPROCESSORS & MICROCONTROLLERS (LAB)	22	48	70	2
7	ANALOG COMMUNICATIONS (LAB)	23	43	66	2	7	DIGITAL SIGNAL PROCESSING (LAB)	23	47	70	2
8	IC APPLICATIONS (LAB)	24	46	70	2	8	ADVANCED ENGLISH COMMUNICATION SKILLS (LAB)	23	44	67	2

IV YEAR

S.No.	I SEMESTER	INT	EXT	TOTAL	CREDITS	S.No.	II SEMESTER	INT	EXT	TOTAL	CREDITS
1	MANAGEMENT SCIENCE	21	33	54	3	1	CELLULAR & MOBILE COMMUNICATIONS	24	36	60	3
2	VLSI DESIGN	24	49	73	4	2	RADAR SYSTEMS	24	31	55	3
3	MICROWAVE ENGINEERING	21	51	72	3	3	WIRELESS COMMUNICATIONS & NETWORKS	24	54	78	3
4	COMPUTER NETWORKS	21	40	61	4	4	INDUSTRY ORIENTED MINI PROJECT	-	44	44	2
5	DIGITAL IMAGE PROCESSING	24	47	71	3	5	SEMINAR	49	-	49	2
6	EMBEDDED SYSTEMS	23	44	67	4	6	MAJOR PROJECT	49	128	177	10
7	E-CAD AND VLSI LAB	24	50	74	2	7	COMPREHENSIVE VIVA	-	95	95	2
8	MICROWAVE ENGG. AND DIGITAL COMMNS. LAB	23	48	71	2						

(# Project Internal= 50, External=150)

Number of Credits registered for : 150   Aggregate Marks Secured for best : 143

Aggregate Marks Secured : 3157 OUT OF 4200 (75.17%)

Date of Issue : June 13, 2014

(see overleaf for Rules concerned to award of class)
A indicates 'ABSENT'

(*Courses registered but not counted for calculation of aggregate)

CONTROLLER OF EXAMINATIONS